



**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Technology and Science**  
**Choice Based Credit System (CBCS) Scheme in the light of NEP-2020**  
**B.Tech. in Electrical Engineering**

**SEMESTER-V (2023-2027)**

SEMESTER-V (2023-2027)												
S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teacher s Assess- ment* (40%)	
1	BTEE503	Control System Engineering	3	1	2	5	60	20	20	30	20	150
2	BTME510	Designing Thinking and Innovation	2	0	0	2	60	20	20	0	0	100
3	BTEE501N	Electrical Machines II	3	1	2	5	60	20	20	30	20	150
4	-	Elective I	3	0	0	3	60	20	20	0	0	100
5	-	Generic Elective II	4	0	0	4	60	20	20	0	0	100
6	BTEEE501	Minor Project	0	0	4	2	0	0	0	30	20	50
7	BTEC507	Programming in Python	0	0	2	1	0	0	0	30	20	50
TOTAL			15	2	10	22	300	100	100	120	80	700
Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;												
*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.												

S. No.	Course Code	Elective I
1	BTEE511	Power Quality
2	BTEE512	Reliability Engineering
3	BTEE514	Introduction of IOT in Electrical Engineering

*[Signature]*

*[Signature]*  
 Chairperson  
 Board of Studies  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore

*[Signature]*  
 Chairperson  
 Faculty of Studies  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore

*[Signature]*  
 Controller of Examinations  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore

*[Signature]*  
 Registrar  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore

*[Signature]*  
 Vice Chancellor  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore